

Applicants: Boris Ginzburget al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-5751-US

Amendments to the Claims

Please amend the claims as follows and cancel the claims marked as cancelled without prejudice:

1. (Currently Amended) A method comprising:
for each of a set of data packets to be transmitted from a wireless communication device to an access point, determining the priority of the data packet as being high priority or not high priority;
scheduling packets determined to be high priority for transmission;
storing data packets determined to be not high priority in a buffer; and
transmitting the data packets determined to be not high priority upon the occurrence of a full buffer condition, the buffer being considered full according to at least one condition selected from the list consisting of: a threshold number of packets stored in the buffer being reached; a threshold size of stored packets being reached; and a threshold aggregated time of transmission for buffered packets being reached.
2. (Previously presented) The method of claim 1, further comprising storing in a buffer said one or more data packets during a power save mode of said wireless communication device.
3. (Previously presented) The method of claim 40, wherein transmitting during an awake mode comprises transmitting said one or more packets in response to a wake-up trigger.
4. (Original) The method of claim 3, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.

Applicants: Boris Ginzburget al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-5751-US

5. (Original) The method of claim 3, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.
6. (Original) The method of claim 3, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
7. (Cancelled)
8. (Cancelled)
9. (Cancelled)
10. (Previously presented) The method of claim 40, wherein transmitting during an awake mode comprises transmitting an awake mode signal to indicate a start of said awake mode.
11. (Previously presented) The method of claim 40, wherein transmitting during an awake mode comprises transmitting a power save signal to indicate an end of said awake mode.
12. (Previously presented) The method of claim 2, comprising disabling a transmitter during said power save mode.
13. (Currently amended) A program storage device having instructions readable by a machine that when executed by the machine result in:
 - for each of a set of data packets to be transmitted from a wireless communication device to an access point, determining the priority of the data packet as being high priority or not high priority; and
 - scheduling packets determined to be high priority for transmission;

Applicants: Boris Ginzburg et al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-5751-US

storing data packets determined to be not high priority in a buffer; and
transmitting the data packets determined to be not high priority upon the
occurrence of a full buffer condition, the buffer being considered full according to
at least one one condition selected from the list consisting of: a threshold number of
packets stored in the buffer being reached; a threshold size of stored packets being
reached; and a threshold aggregated time of transmission for buffered packets being
reached.

14. (Previously presented) The program storage device of claim 13, wherein said instructions further result in storing in a buffer said one or more data packets during a power save mode of said wireless communication device.
15. (Previously presented) The program storage device of claim 41, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting said one or more packets in response to a wake-up trigger.
16. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate anticipated transmission time of the one or more data packets.
17. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to an aggregate size of the one or more data packets.
18. (Original) The program storage device of claim 15, wherein said wake-up trigger relates to a period of time during which no data packets are sent for transmission.
19. (Cancelled)

Applicants: Boris Ginzburget al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-5751-US

20. (Cancelled)
21. (Cancelled)
22. (Previously presented) The program storage device of claim 41, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting an awake mode signal to indicate a start of said awake mode.
23. (Previously presented) The program storage device of claim 41, wherein the instructions that result in transmitting during an awake mode comprise instructions that result in transmitting a power save signal to indicate an end of said awake mode.
24. (Previously presented) The program storage device of claim 14, wherein the instructions result in disabling a transmitter during said power save mode.
25. (Currently Amended) An apparatus comprising:
- a buffer to store one or more data packets determined to be not high priority; and
 - a transmitter operatively coupled to said buffer, said transmitter to transmit data packets determined to be high priority and, upon the occurrence of a full buffer condition, data packets determined to be not high priority;
- wherein the buffer is considered full according to at least one condition selected from the list consisting of: a threshold number of packets stored in the buffer being reached; a threshold size of stored packets being reached; and a threshold aggregated time of transmission for buffered packets being reached.

Applicants: Boris Ginzburget al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-5751-US

26. (Previously presented) The apparatus of claim 25, further comprising a processor adapted to transmit an awake signal to indicate a start of an awake mode.
27. (Original) The apparatus of claim 26, wherein said processor is further adapted to transmit a power save signal to indicate an end of said awake mode.
28. (Original) The apparatus of claim 27, comprising a disabling unit to disable said transmitter during said power save mode.
29. (Original) The apparatus of claim 28, wherein said disabling unit is able to enable said transmitter during said power save mode.
30. (Currently Amended) A wireless communication device comprising:
a buffer to store one or more data packets determined to be not high priority; and
a transmitter operatively coupled to said buffer, said transmitter to transmit data packets determined to be high priority and, upon the occurrence of a full buffer condition, data packets determined to be not high priority; and
an omni-directional antenna operationally coupled to said transmitter;
wherein the buffer is considered full according to at least one condition selected from the list consisting of: a threshold number of packets stored in the buffer being reached; a threshold size of stored packets being reached; and a threshold aggregated time of transmission for buffered packets being reached.
31. (Original) The wireless communication device of claim 30, further comprising a processor to produce said one or more data packets.

Applicants: Boris Ginzburget al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-5751-US

32. (Previously presented) The wireless communication device of claim 30, wherein said transmitter is further adapted to transmit an awake mode signal to indicate a start of an awake mode.
33. (Previously presented) The wireless communication device of claim 32, wherein said transmitter is further adapted to transmit a power save mode signal to indicate an end of said awake mode.
34. (Previously presented) The wireless communication device of claim 32, further comprising a power source and circuitry to connect said transmitter to said power source during said awake mode.
35. (Previously Presented) The wireless communication device of claim 34, further comprising circuitry to disconnect said transmitter from said power source during a power save mode.
36. (Currently Amended) A wireless communication system comprising:
a first wireless communication device adapted to:
 determine the priority of each of a set of data packets to be transmitted from said first wireless communication device to an access point, as being high priority or not high priority;
 schedule packets determined to be high priority for transmission;
 store data packets determined to be not high priority in a buffer; and
 transmit the data packets determined to be not high priority upon the occurrence of a full buffer condition, the buffer being considered full according to at least one condition selected from the list consisting of: a threshold number of packets stored in the buffer being reached; a threshold size of stored packets being reached; and a threshold aggregated time of transmission for buffered packets being reached; and

Applicants: Boris Ginzburget al.
Serial Number: 10/608,143

Assignee: Intel Corporation
Attorney Docket: P-3751-US

a second wireless device adapted to receive said one or more data packets.

37. (Previously presented) The wireless communication system of claim 36, wherein said second wireless device is further adapted to transmit during an awake mode one or more data packets sent for transmission during a power save mode.
38. (Previously presented) The wireless communication system of claim 37, wherein said first wireless device is further adapted to transmit an awake mode signal to indicate a start of an awake mode.
39. (Previously Presented) The wireless communication system of claim 38, wherein said first wireless device is further adapted to transmit a power save mode signal to indicate an end of said awake mode.
40. (Previously presented) The method of claim 1, wherein transmitting occurs during an awake mode of said wireless communication device.
41. (Previously presented) The program storage device of claim 13, wherein the instructions result in transmitting during an awake mode of said wireless communication device.